

PATENT

DOCKET NO. 30585/9

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): John S. Yates, Jr., et al.

Serial No.: 09/425,401

Art Unit: 2751

Filed: October 22, 1999

Examiner: unassigned

PROFILING PROGRAM EXECUTION TO IDENTIFY FREQUENTLY
EXECUTED PORTIONS AND TO ASSIST BINARY TRANSLATION

ASSISTANT COMMISSIONER FOR PATENTS
Washington, D.C. 20231



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INFORMATION DISCLOSURE STATEMENT

In accordance with 37 C.F.R. §§1.56, 1.97 and 1.98, Applicant wishes to make certain items of record, as listed on the accompanying Form PTO-1449. Applicant respectfully requests the Examiner to consider the items before issuance of the next action, and to make them of record in the file. The Examiner is also requested to initial and return a copy of the enclosed Form PTO-1449 to evidence such consideration.

Of the references listed on the Form 1449, it is believed that the papers by Chernoff, Conte, Cox, Hank, and Menezes are the most relevant. The Examiner is requested to fully consider all items and independently ascertain their teaching.

Copies of the items listed on the enclosed Form PTO-1449 that are not enclosed with this Information Disclosure Statement were previously submitted to the Patent and Trademark Office in Information Disclosure Statements filed in application Serial No. 09/239,194, Yates et al., EXECUTING PROGRAMS FOR A FIRST COMPUTER ARCHITECTURE ON A COMPUTER OF A SECOND ARCHITECTURE, filed January 28, 1999, and in application Serial No. 09/322,443, Reese, et al., PROFILING OF COMPUTER PROGRAMS EXECUTING IN VIRTUAL MEMORY SYSTEMS, filed May 28, 1999.

Other items, not listed on the enclosed Form PTO-1449, have been submitted to the Patent and Trademark Office in application Serial No. 09/239,194, Yates et al., EXECUTING PROGRAMS FOR A FIRST COMPUTER ARCHITECTURE ON A COMPUTER OF A

I certify that this correspondence, along with any documents referenced therein, is being deposited with the United States Postal Service on December 14, 1999 with sufficient postage as First Class Mail, addressed to The Assistant Commissioner of Patents, Washington D.C. 20231.

[Signature]

SECOND ARCHITECTURE, filed January 28, 1999, application Serial No. 09/348,317, Yates et al., RECORDING CLASSIFICATION OF INSTRUCTIONS EXECUTED BY A COMPUTER, filed July 7, 1999, and application Serial No. 09/385,394, Yates et al., COMPUTER FOR EXECUTING TWO DIFFERENT INSTRUCTION SETS, filed August 30, 1999. It is believed that none of the references made of record in the '194, '317, or '394 applications are pertinent to the independent claims of the current application, except those that are made of record in this application and listed in this Form 1449. Nonetheless, the Examiner is requested to be aware of these additional items.

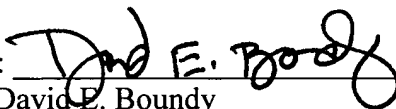
No fee is due under 37 C.F.R. §1.17(p) for this Information Disclosure Statement since it is being filed in compliance with 37 C.F.R. §1.97(b)(3), to the knowledge of the undersigned, before the mailing date of a first Office Action on the merits.

The Assistant Commissioner is hereby authorized to charge any additional fees that may be required for this Information Disclosure Statement, or credit any overpayment, to Deposit Account 50-0324, Order No. 30585/9.

Respectfully submitted,

SHEARMAN & STERLING

Dated: December 14, 1999

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